

# AFBR-3905xxRZ

High Voltage Galvanic Insulation Link for DC to 5MBaud



## Data Sheet



### Description

Avago Technologies' AFBR-3905xxZ is a high voltage galvanic insulation link for DC to 5MBaud. The AFBR-3905xxZ consists of an optical transmitter and receiver operating at 650nm wavelength. Pin to pin distance of approximately 25 to 101 mm provides transient voltage suppression in the range of 15kV to 50kV.

### Applications

- Drives/Inverters
- Galvanic insulation on one single PCB
- Medium Voltage Power Distributions
- Regulated Distribution Transformers
- Smart Grid on-board Insulations

### Ordering Information

Part Number	Length	mm	Voltage Suppression
AFBR-390525RZ	1 inch	25	15kV
AFBR-390550RZ	2 inch	50.4	27kV
AFBR-390575RZ	3 inch	75.8	40kV
AFBR-390500RZ	4 inch	101.2	50kV

### Features

- Data transmission at signal rates of DC to 5MBaud
- DC coupled receiver with CMOS/TTL output for easy designs: no data encoding or digitizing circuitry required
- High noise immunity through receiver IC with integrated photodiode
- RoHS compliant
- Transient voltage suppression in the range of 15kV to 50kV according IEC 60644
- Laser class 1 according to IEC-60825
- Tested according to IEC-60747-5-5
- Housing Material UL-V0 with CTI  $\geq$  600
- Optional 3.3V or 5V power supply

## AFBR-3905xxRZ DC to 5MBaud Data Link

### Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units
Signaling Rate	$f_s$	DC	5	MBd
Storage and Operating Temperature	$T_{S,O}$	-40	+85	°C
Receiver Supply Voltage	$V_{DD}$	-0.5	+5.5	V
Receiver Output Current	$I_{OAV}$		10	mA
Transmitter Peak Forward Input Current	$I_{F,PK}$		30	mA
Transmitter Reverse Input Voltage	$V_R$		3	V
Lead Soldering Cycle <sup>[1, 2]</sup>	Temp	$T_{SOL}$	+260	°C
	Time		10	sec

Notes:

1. 1.6mm below seating plane; wave soldering only
2. MSL class 3

### Attention

Stresses above those listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Units
Ambient Temperature	$T_A$	-40	85	°C
Rx Power Supply Voltage <sup>[1]</sup>	$V_{CC}$	3.135	3.465	V
		4.75	5.25	V
Transmitter Average Forward Current	$I_{F,AV}$	5	10	mA
Signaling Rate	$f_s$	DC	5	MBd

Notes:

1. <100mVp-p Noise

**All the data in this specification refers to the operating conditions above and over lifetime unless otherwise stated.**

## Insulation Characteristics

Parameter	Symbol	Min.	Max.	Units
Apparent charge at Sample Test stage and Type Test stage after subgroup 1 (method a) [1]	q <sub>pd</sub>		5	pC
Apparent charge at Routine Test stage and Type Test stage, Preconditioning (method b) [2]	q <sub>pd</sub>		5	pC
Maximum Transient Voltage, peak [3]	V <sub>IOTM_1inch</sub> V <sub>IOTM_2inch</sub> V <sub>IOTM_3inch</sub> V <sub>IOTM_4inch</sub>	15 27 40 50		kV
Maximum Transient Voltage, effective [3]	V <sub>ISO_1inch</sub> V <sub>ISO_2inch</sub> V <sub>ISO_3inch</sub> V <sub>ISO_4inch</sub>	10.5 19 28.1 35.2		kV
Maximum Working Voltage, peak [4]	V <sub>IORM_1inch</sub> V <sub>IORM_2inch</sub> V <sub>IORM_3inch</sub> V <sub>IORM_4inch</sub>	4.25 8.5 12.75 17.00		kV
Maximum Working Voltage, effective [4]	V <sub>IOWM_1inch</sub> V <sub>IOWM_2inch</sub> V <sub>IOWM_3inch</sub> V <sub>IOWM_4inch</sub>	3 6 9 12		kV
Insulation Resistance @ T <sub>amb,max</sub> , min.100°C	R <sub>IO</sub>	10 <sup>11</sup>		Ω
Insulation Resistance @ T <sub>S</sub>	R <sub>IO</sub>	10 <sup>9</sup>		Ω
Creepage Distance	1inch 2inch 3inch 4inch	25 50.4 75.8 101.2		mm
Clearance Distance	1inch 2inch 3inch 4inch	25 50.4 75.8 101.2		mm
Surge Isolation Voltage	V <sub>IOSM</sub>	12		kV
Comparative Tracking Index	CTI	600		
Pollution degree [5]		2		
Climatic category [6]		40/085/21		
Maximum ambient Safety temperature	T <sub>S</sub>	110		°C
Maximum input current	I <sub>SI</sub>	60		mA
Maximum output current	I <sub>SO</sub>	30		mA
Maximum input power dissipation	P <sub>SI</sub>	330		mW
Maximum output power dissipation	P <sub>SO</sub>	165		mW

### Notes:

1.  $V_{pd(m)} = 1.6 \times V_{IORM}$  (=6.8kV for 1inch, =13.6kV for 2inch, =20.4kV for 3inch, =27.2kV for 4inch),  $V_{ini,a} = V_{IOTM}$ ,  $t_{ini,a} = 60s$ ;  $t_m = 10s$
2.  $V_{pd(m)} = 1.875 \times V_{IORM}$  (=8kV for 1inch, =16kV for 2inch, =24kV for 3inch, =32kV for 4inch),  $V_{ini,b} = V_{IOTM}$ ,  $t_{ini,b} = 1s$ ;  $t_m = 1s$
3. Altitude up to 2000m above sea level
4. Pollution degree 2; please note that inhomogeneous field conditions may lead to partial discharge through air for these voltages
5. According IEC-60064-1
6. According IEC-60068-1

## Electrical Input Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Forward Voltage <sup>[1]</sup>	$V_F$	1.6		2.2	V
Forward Voltage Temperature Coefficient	$\Delta V_F / \Delta T$		-1.8		mV/°C
Reverse Input Breakdown Voltage <sup>[2]</sup>	$V_{BR}$	3.0	13		V
Diode Capacitance <sup>[3]</sup>	$C_0$		30		pF

Notes:

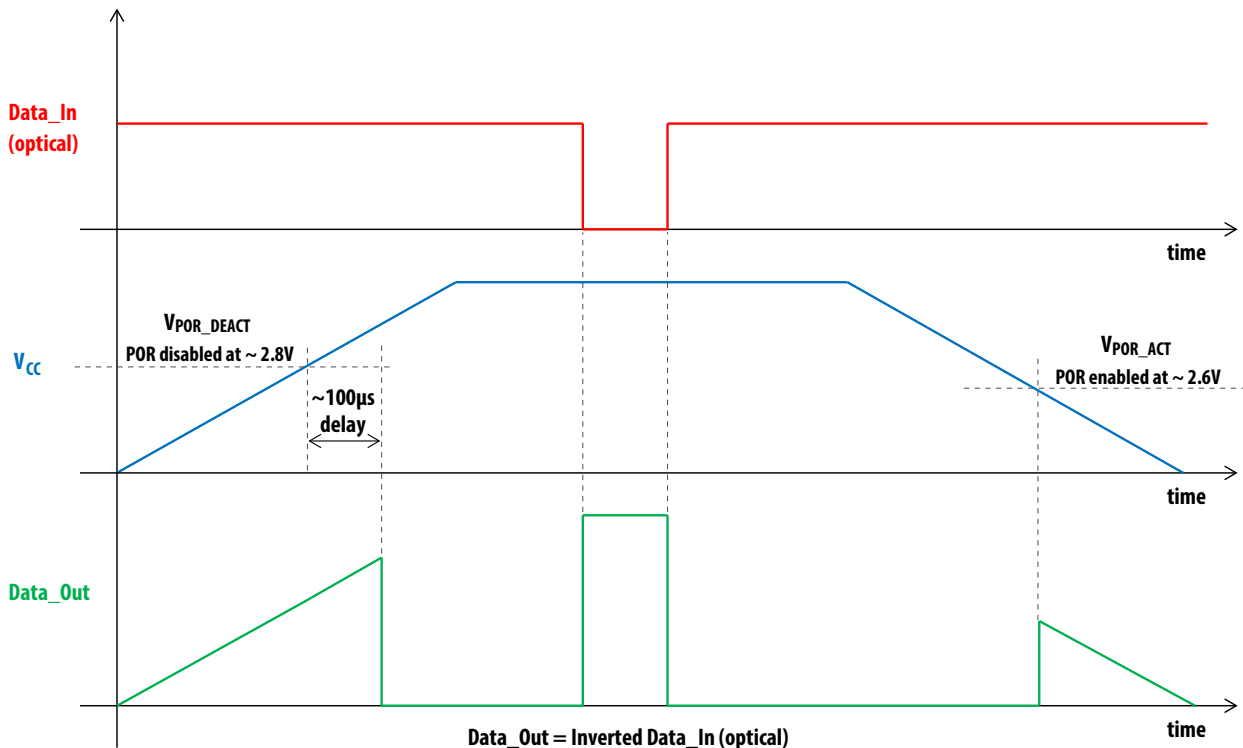
1.  $I_{F,dc} = 10\text{mA}$
2.  $I_{F,dc} = -10\mu\text{A}$
3.  $V_F = 0\text{V}; f = 1\text{MHz}$

## Electrical Output Signal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
High Level Output Voltage	$V_{OH}$	2.5	$V_{CC}$	$V_{CC}+0.3$	V
Low Level Output Voltage	$V_{OL}$		0.22	0.4	V
Output Risetime (10-90%) <sup>[1, 2]</sup>	$t_r$			10	ns
Output Falltime (90-10%) <sup>[1, 2]</sup>	$t_f$			10	ns
Power Supply Noise Immunity <sup>[3]</sup>	PSNI	0.1	0.4		Vpp
Vcc level to deactivate POR <sup>[4]</sup>	$V_{POR\_DEACT}$		2.8		V
Vcc level to activate POR <sup>[4]</sup>	$V_{POR\_ACT}$		2.6		V
POR deactivate delay time <sup>[4]</sup>	$t_{POR\_DEACT\_DEL}$		100		$\mu\text{s}$

Notes:

1.  $C_L = 20\text{pF}, R_L = 50\text{k}\Omega$
2. In the recommended drive circuit
3. Peak-to-peak sine wave
4. Power-on reset (POR) is active below  $V_{POR\_DEACT}$ . Once  $V_{POR\_DEACT}$  is reached the POR remains active for  $t_{POR\_DEACT\_DEL}$ . During power down POR starts at  $V_{POR\_ACT}$ .



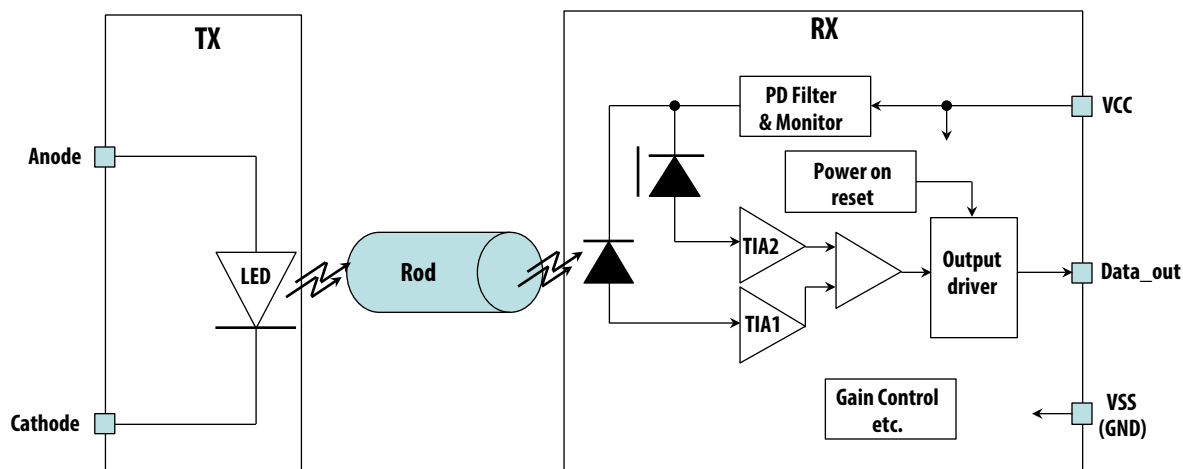
**Specified Link Performance,  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ , DC to 5MBaud, unless otherwise noted.**

Parameter	Symbol	Min.	Typ	Max.	Unit	Condition
Signaling Rate	$f_S$	DC		5	MBd	NRZ
Pulse Width Distortion [1]	PWD	-30		30	ns	5MBaud
Propagation Delay [2]	$t_D$			80	ns	5MBaud
Skew [3]	$t_S$			20	ns	5MBaud
Supply Current Rx [4]	$I_{CC}$		6	10	mA	

Notes:

1.  $\pm 15\%$  of the nominal pulse width, provided no pulse width distortion at the electrical input
2. determined from 50% of the rising edge of data\_in to 50% of the consecutive rising edge of data\_out
3. Variations of  $t_D$  between multiple devices measured for same input conditions and same external signal delay
4.  $C_L = 20\text{pF}$ ,  $R_L = 50\text{k}\Omega$

**Block Diagram - AFBR-3905xxRZ**



The Rx Data\_out signal is inverted which means that light\_on will lead to Data\_out low.

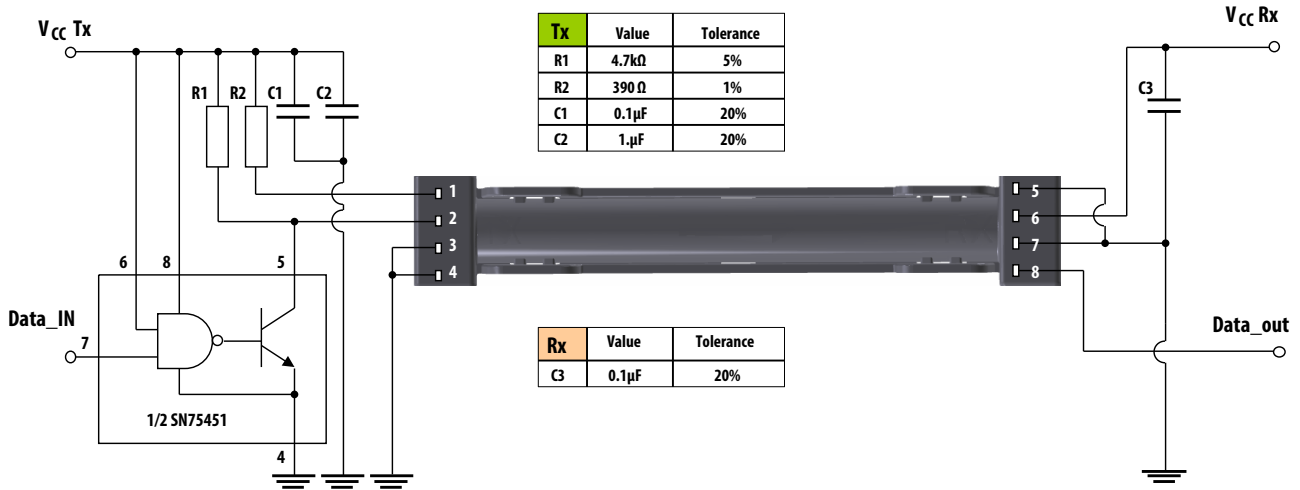
POR remains active during  $V_{CC}$  power up, typically until  $100\mu\text{s}$  after 2.8V is reached. POR follows  $V_{CC}$  while active.

**Recommended chemicals for Cleaning/Degreasing**

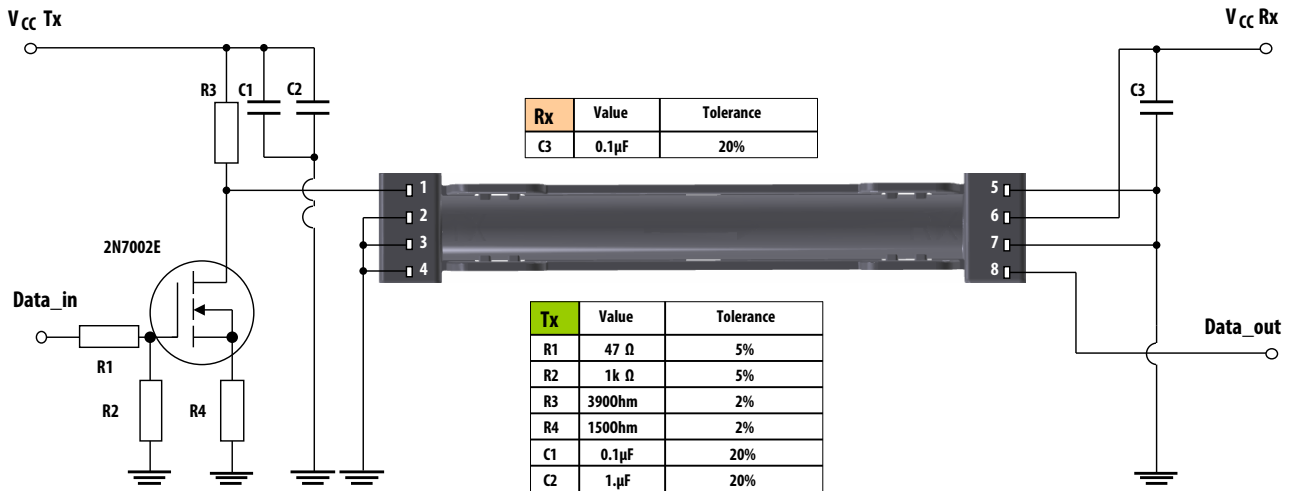
Alcohols: methyl, isopropyl, isobutyl.  
 Aliphatics: hexane, heptanes  
 Other: soap solution, naphtha

Do not use partially halogenated hydrocarbons such as 1.1.1 trichloroethane, ketones such as MEK, acetone, chloroform, ethyl acetate, methylene dichloride, phenol, methylene chloride, or N-methylpyrrolidone. Also, Avago does not recommend the use of cleaners that use halogenated hydrocarbons because of their potential environmental harm.

### Recommended Drive Circuit (a) – Top View



### Recommended Drive Circuit (b) – Top View



### Pin Description

Pin number	Transmitter	Pin number	Receiver
1	Anode	5	No function <sup>[1]</sup>
2	Cathode	6	VCC
3	No function <sup>[1]</sup>	7	GND
4	No function <sup>[1]</sup>	8	Data_out

Notes:

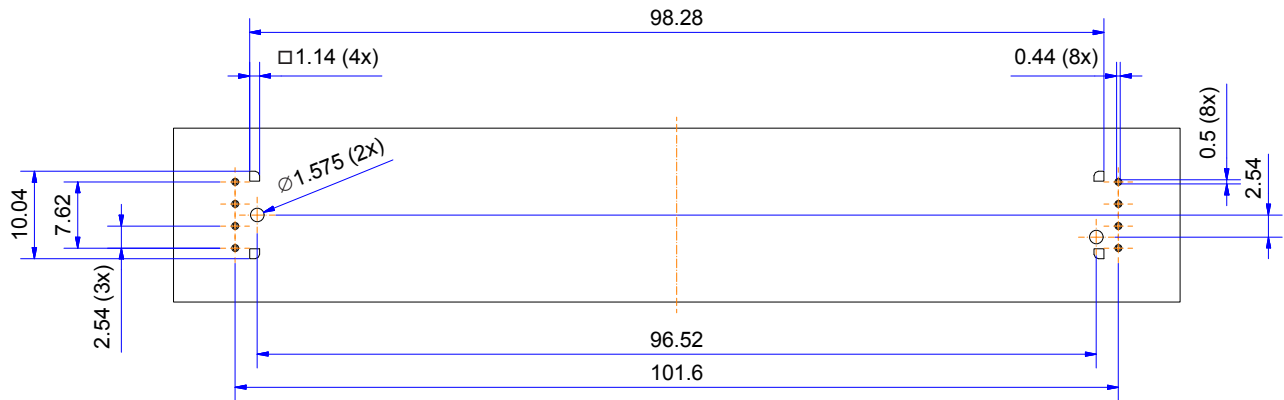
1. It is recommended to connect this pin to signal ground



## Footprint (Top View)

Dimensions in mm

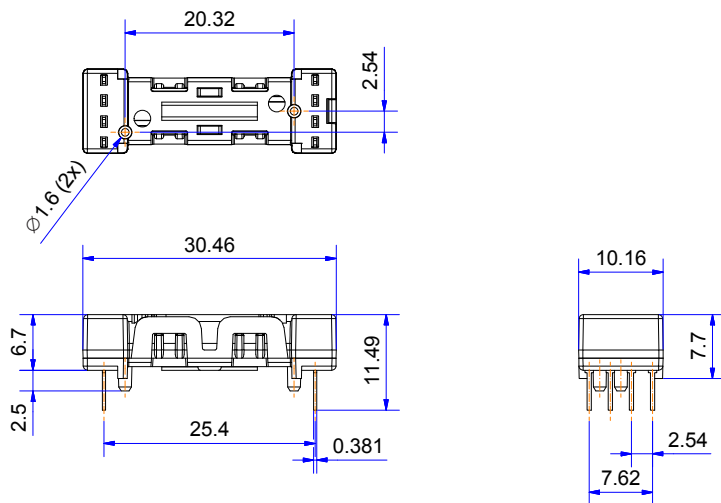
### AFBR-390500RZ



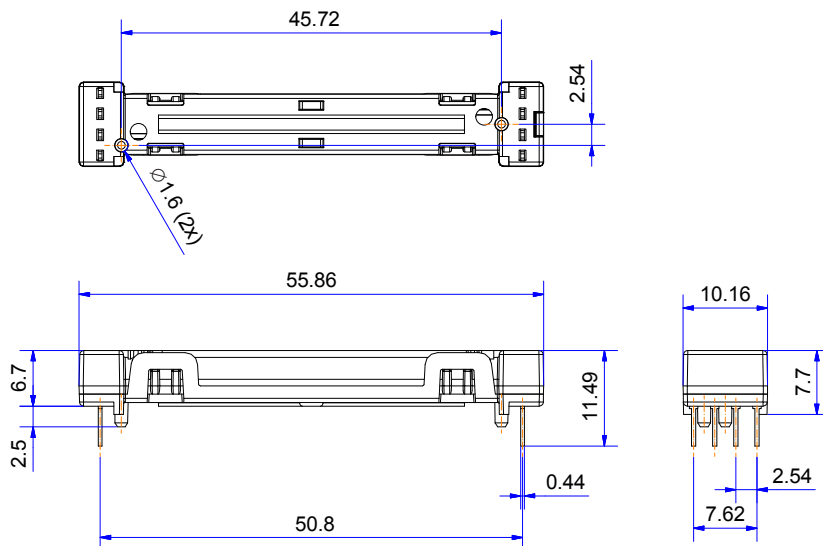
## Mechanical Dimensions

Dimensions in mm

### AFBR-390525RZ



### AFBR-390550RZ

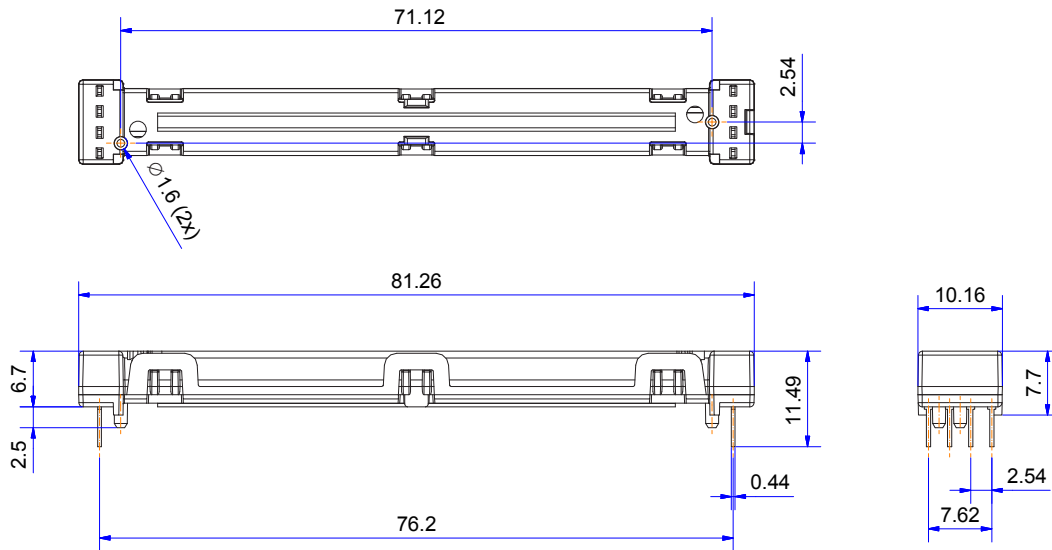




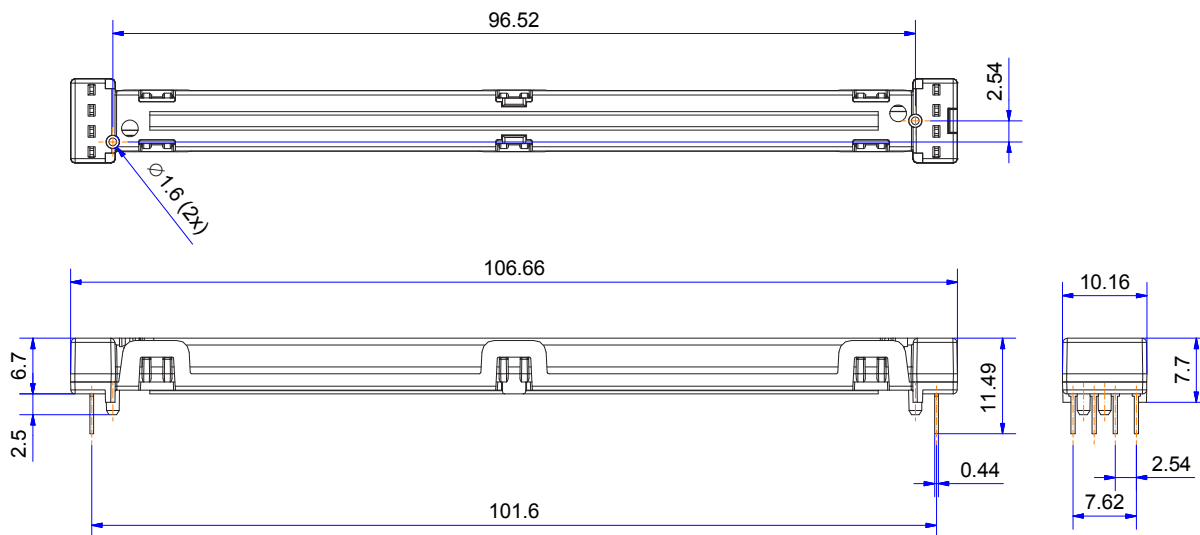
## Mechanical Dimensions

Dimensions in mm

### AFBR-390575RZ



### AFBR-390500RZ



### IMPORTANT NOTE:

**AFBR-3905xxRZ devices must not be bent under any circumstances.**

For product information and a complete list of distributors, please go to our web site: [www.avagotech.com](http://www.avagotech.com)

Avago, Avago Technologies, and the A logo are trademarks of Avago Technologies in the United States and other countries. Data subject to change. Copyright © 2005-2015 Avago Technologies. All rights reserved. AV02-4872EN - April 2, 2015

**AVAGO**  
TECHNOLOGIES